


NOTES : UNLESS OTHERWISE SPECIFIED

1. MANUFACTURE IN ACCORDANCE WITH IPC-6012, TYPE 3, CLASS 2.
2. END PRODUCT FEATURES SHALL NOT VARY MORE THAN 20% FROM ARTWORK ORIGINALS.
3. MATERIAL SHALL BE COPPER CLAD FR-4, NEMA GRADE PER IPC-4101/26, COLOR NATURAL.
4. COPPER WEIGHT SHALL BE 1.0 OZ./SQ. FT. BEFORE PLATING.
5. ALL PLATED THROUGH HOLES SHALL HAVE A MINIMUM OF 0.001" COPPER.
6. DRILL HOLE TOLERANCE AFTER PLATING SHALL BE ± 0.003 ".
7. MINIMUM ANNULAR RING SHALL BE 0.001".
8. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE 0.003".
9. FINAL PCB THICKNESS SHALL BE 0.062" $\pm 10\%$.
10. WARP/TWIST SHALL NOT EXCEED 1.0%.
11. FINISH SHALL BE LPI, RED SOLDER MASK OVER OXIDIZED COPPER, HASL BOTH SIDES.
12. SILKSCREEN WITH NONCONDUCTIVE WHITE EPOXY INK.
13. REFERENCE ADDITIONAL FAB NOTES IN FILE README.TXT

LAYER STACKUP	FILE NAMES
PRIMARY SILKSCREEN	7013EB-UDP_PSS.PHO
PRIMARY SOLDERMASK	7013EB-UDP_PSM.PHO
PRIMARY SIDE	7013EB-UDP_PRI.PHO
SECONDARY SIDE	7013EB-UDP_SEC.PHO
SECONDARY SOLDERMASK	7013EB-UDP_SSM.PHO
SCALE: NONE	

SIZE	QTY	SYM	PLT	TOOL	TOL
0.015	63	+	P	1	+0/-0.015
0.040	20	X	P	2	+/-0.003

UNLESS OTHERWISE SPECIFIED			THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SHALL NOT BE DUPLICATED OR USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED IN WHOLE OR IN PART, WITHOUT THE WRITTEN CONSENT OF SILICON LABORATORIES, INC..			COMPANY:		<div></div> <div>SILICON LABS</div> <div>400 W Cesar Chavez AUSTIN, TX 78701 (512)416-8500 www.silabs.com</div>			
DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH DIMENSIONS IN BRACKETS [] ARE IN MILLIMETERS INTERPRET DRAWING PER MIL-D-1000											
TOLERANCES											
HOLE TOLERANCES PER 78027											
DECIMALS .XX +/- .XXX +/-	ANGLES +/-	SURFACES <div><input checked="" type="checkbox"/> MICROINCHES</div>				NAME:		Si7013EB-UDP		REV : 1.0	
PART TO BE FREE OF BURRS			DESIGN			JG	22APR2013			SIZE	PART NUMBER:
			LAYOUT			CT	22APR2013			A	
BREAK EDGES MAX	BEND RADIUS MAX	BEND RELIEF MAX	DO NOT SCALE DRAWING			SCALE 1:1		FABRICATION DRAWING		SHEET 1 OF 1	

Si7013EB-UDP REV 1.0

Size:

Array: 5.0 x 6.275

Part: 1.0 x 1.925

Parts On Array:

15 Parts

Matrix:

5 x 3

Spacing:

0.0 x 0.0

Array Borders:

Left: 0.0 Right: 0.0

Top: 0.25 Bottom: 0.25

Notes:

Please add 4, 0.125" NP tooling holes located 0.125" from tab corners and 3, 40/120 fiduials located 0.25" from tooling holes.